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3. Copies of the certified copies of the priority documents have been received in this national stage application from the	
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Application/Control Number: 09/905,474

Art Unit: 2827

DETAILED ACTION

Allowable Subject Matter

- Claims 1-31 have been allowed.
- 2. The following is an examiner's statement of reasons for allowance:

As to Claims 1-8 and 17-24, patentability resides in the capacitor is operably disposed on the ground pad the capacitor is operably disposed on the ground pad a via electrically coupling the ground pad to the second voltage plane, in further combination with the other limitations of base Claims 1 and 17, respectively.

As to Claims 9-16 and 25-31, patentability resides in the combination wherein: the capacitor is operably disposed on the ground pad, a trace electrically coupled to the ground plane and a plurality of vias electrically coupling the trace to the second voltage plane, in further combination with the other limitations of base Claims 9 and 25, respectively.

3. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

 The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

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- b) Rowe et al. (US 4,739,448) discloses, in Fig. 3, a ground pad 66 with capacitor 64 and a voltage pad 58, including voltage sections at the end of vias 78 and 102, including ground sections 90 and 138. Nevertheless, this reference does not disclose or suggest that the ground pad be coupled to the second voltage plane. Ground pad 66 is indeed coupled with vias 88 and 106 to portions of the planes that include the voltage sections (sections under vias 78 and 102), but the via specifically couples to the ground portions 90 and not the voltage portions.
- c) Ninomiya (US 2003/0024732 A1) discloses, in Fig. 19, capacitor 24 with voltage layers 23 and ground layers G. Nevertheless, the connection of the capacitor to a ground pad wherein that pad is coupled with a via to a second voltage plane does not exist.
- d) The other cited references to Moresco (US 6,535,398 B1: Figs. 7 and 8) and Harada et al. (US 6,512,680 B2: Fig. 4) show some of the elements of the claim such as a capacitor or voltage or ground planes. But none of them alone or in a permissible combination disclose all the limitations of the various planes connected as claimed in the instant Application.
- Any inquiry concerning this communication or earlier communications from the examiner should be directed to John B. Vigushin whose telephone number is 703-308-1205. The examiner can normally be reached on 8:30AM-5:00PM Mo-Fri.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David L. Talbott can be reached on 703-305-9883. The fax phone numbers for the organization where this application or proceeding is assigned are 703-308-7382 for regular communications and 703-308-7382 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

John B. Vigush Examiner Art Unit 2827

jbv June 29, 2003